L Number	Hits	Search Text	DB	Time stamp
-	0	("fuzzynearunionnearintersectionnearcircuit").PN.	USPAT;	2003/09/30 08:59
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
- '	0	fuzzy near union near intersection near circuit	USPAT;	2003/09/30 08:59
			US-PGPUB;	
ļ			EPO; JPO;	
			DERWENT;	i
İ			IBM_TDB	
-	94	fuzzy and union and intersection and circuit	USPAT;	2004/05/08 19:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	31	(fuzzy and union and intersection and circuit) and	USPAT;	2004/05/08 19:52
		@pd<=19981130	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
-	1041	(fuzzy and min and max and circuit) and @pd<=19981130	USPAT;	2004/05/08 19:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	27	((fuzzy and min and max and circuit) and @pd<=19981130)	USPAT;	2004/05/08 19:52
		and subtracter	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	213	fuzzy and union and circuit	USPAT;	2004/05/08 19:53
			US-PGPUB;	
ļ			EPO; JPO;	:
			DERWENT;	
	04	/6 and union and singuity and interception	IBM_TDB	2002/00/20 46 57
-	94	(fuzzy and union and circuit) and intersection	USPAT;	2003/09/30 16:57
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	2	((fizzy and union and circuit) and interpretion) and	IBM_TDB	2004/05/09 10-52
-	_	((fuzzy and union and circuit) and intersection) and subtracter	USPAT;	2004/05/08 19:53
		Jubu detel	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
-	0	((fuzzy and union and circuit) and intersection) and	USPAT;	2003/09/30 16:59
		exclusive-or	US-PGPUB;	2003/03/30 10.39
			EPO; JPO;	
j l			DERWENT;	
			IBM_TDB	
-	2	((fuzzy and union and circuit) and intersection) and	USPAT;	2004/05/08 19:53
	_	(exclusive adj "or")	US-PGPUB;	200.,00,00 25.00
		<b>,</b>	EPO; JPO;	
[			DERWENT;	
			IBM_TDB	
-	38	(fuzzy and min and max and circuit) and subtracter	USPAT;	2004/05/08 19:53
		· · · · · · · · · · · · · · · · · · ·	US-PGPUB;	, , , , , , , , , , , , , , , , ,
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

-	2	((fuzzy and min and max and circuit) and subtracter) and	USPAT;	2004/05/08 19:54
		mux	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	9	((fuzzy and min and max and circuit) and subtracter) and	USPAT;	2004/05/08 19:54
		multiplexer	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	129	(706/1).ccls.	USPAT;	2003/09/30 17:49
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	24	(706/6).ccls.	USPAT;	2003/09/30 17:49
			US-PGPUB;	,
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	95	((706/1).ccls.) and circuit	USPAT;	2003/09/30 18:00
		((700/2)/00/01/ 01/00/01/01	US-PGPUB;	2003/03/30 10:00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	11	((706/6).ccls.) and circuit	USPAT;	2003/09/30 17:50
	]	((700/0).ccis.) and circuit	US-PGPUB;	2003/03/30 17.30
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	28	(((706/1).ccls.) and circuit) and multiplexer	USPAT;	2003/09/30 17:52
_	20	(((700/1).ccis.) and circuity and multiplexer	US-PGPUB;	2003/03/30 17.32
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	5	((((706/1).ccls.) and circuit) and multiplexer) and subtracter	USPAT;	2003/09/30 17:52
_		((((700/1).ccis.) and circuit) and multiplexer) and subdacter	US-PGPUB;	2003/03/30 17.32
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	1	((((706/1) ccls.) and circuit) and multipleyer) and	USPAT;	2003/09/30 17:53
_		((((((706/1).ccls.) and circuit) and multiplexer) and subtracter) and "xor"	1	2003/03/30 17.33
		Subulacter   and   Add	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	0	(((((706/1) cele ) and circuit) and multipleyer) and	IBM_TDB USPAT;	2003/09/30 17:53
•	١	(((((706/1).ccls.) and circuit) and multiplexer) and	,	2003/03/30 17.33
		subtracter) and exclusive-or	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		(((((706/1) cole ) and circuit) and multiplanes and	IBM_TDB	2002/00/20 47:50
-	1	(((((706/1).ccls.) and circuit) and multiplexer) and	USPAT;	2003/09/30 17:53
		subtracter) and (exclusive adj "or")	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_	("C20EE00") DN	IBM_TDB	2002/00/20 40 07
-	2	("6385598").PN.	USPAT;	2003/09/30 18:05
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
··	<u>L</u>		IBM_TDB	

-	2	("6424958").PN.	USPAT;	2003/09/30 18:12
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
<u>-</u>	. 26	PAPPALARDO-FRANCESCO .in.	USPAT;	2003/09/30 18:12
	20	FAFFADARDO-I MINCESCO .III.	· · · · · · · · · · · · · · · · · · ·	2003/03/30 16.12
	!		US-PGPUB;	
	;		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	26	PAPPALARDO-FRANCESCO.in.	USPAT;	2004/05/08 21:51
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2	("5720005").PN.	USPAT;	2003/09/30 18:39
	_	(	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			'	
	-	/#30030000673##300300E30E7##300300E04 40##6 43 4050#	IBM_TDB	1004/05/00 40-36
-	7	("20020099673" "20020052857" "20020059149" "6424958"	USPAT;	2004/05/08 18:36
		"5915247" "5875438" "6385598").did.	US-PGPUB	
-	14	("20020099673" "20020052857" "20020059149" "6424958"	USPAT;	2004/05/08 18:40
		"5915247" "5875438" "6385598" "6075338" "6424957"	US-PGPUB	
		"5943664" "5806051" "5796917" "5710867" "5615303").did.		
-	1	fuzzy and union and intersection and circuit and	USPAT;	2004/05/08 19:52
		(("20020099673" "20020052857" "20020059149" "6424958"	US-PGPUB;	
		"5915247" "5875438" "6385598" "6075338" "6424957"	EPO; JPO;	
		"5943664" "5806051" "5796917" "5710867"	DERWENT;	
		"5615303").did.)	IBM_TDB	
_	1	(fuzzy and union and intersection and circuit) and	USPAT;	2004/05/08 19:52
	1	@pd<=19981130 and (("20020099673" "20020052857"	US-PGPUB;	200 1/03/00 13.52
		"20020059149" "6424958" "5915247" "5875438" "6385598"	•	
	]		EPO; JPO;	
		"6075338" "6424957" "5943664" "5806051" "5796917"	DERWENT;	
	_	"5710867" "5615303").did.)	IBM_TDB	0004/05/00 40 50
-	3	(fuzzy and min and max and circuit) and @pd<=19981130	USPAT;	2004/05/08 19:52
		and (("20020099673" "20020052857" "20020059149"	US-PGPUB;	
		"6424958" "5915247" "5875438" "6385598" "6075338"	EPO; JPO;	
		"6424957" "5943664" "5806051" "5796917" "5710867"	DERWENT;	
		"5615303").did.)	IBM_TDB	
-	0	((fuzzy and min and max and circuit) and @pd<=19981130)	USPAT;	2004/05/08 19:52
		and subtracter and (("20020099673" "20020052857"	US-PGPUB;	
		"20020059149" "6424958" "5915247" "5875438" "6385598"	EPO; JPO;	
		"6075338" "6424957" "5943664" "5806051" "5796917"	DERWENT;	]
		"5710867" "5615303").did.)	IBM TDB	
-	1	fuzzy and union and circuit and (("20020099673"	USPAT;	2004/05/08 19:53
	1	"20020052857" "20020059149" "6424958" "5915247"	US-PGPUB;	
		"5875438" "6385598" "6075338" "6424957" "5943664"	EPO; JPO;	
		"5806051" "5796917" "5710867" "5615303").did.)	DERWENT;	
		//6.m., and union and singuish and interesting and	IBM_TDB	2004/05/00 40:52
_	0	((fuzzy and union and circuit) and intersection) and	USPAT;	2004/05/08 19:53
		subtracter and (("20020099673" "20020052857"	US-PGPUB;	
		"20020059149" "6424958" "5915247" "5875438" "6385598"	EPO; JPO;	
		"6075338" "6424957" "5943664" "5806051" "5796917"	DERWENT;	
	1	"5710867" "5615303").did.)	IBM_TDB	
-	0	((fuzzy and union and circuit) and intersection) and	USPAT;	2004/05/08 19:53
		(exclusive adj "or") and (("20020099673" "20020052857"	US-PGPUB;	
		"20020059149" "6424958" "5915247" "5875438" "6385598"	EPO; JPO;	
		"6075338" "6424957" "5943664" "5806051" "5796917"	DERWENT;	
		"5710867" "5615303").did.)	IBM TDB	
	<u> </u>		1 100	

-	1	(fuzzy and min and max and circuit) and subtracter and	USPAT;	2004/05/08 19:54
		(("20020099673" "20020052857" "20020059149" "6424958"	US-PGPUB;	
		"5915247" "5875438" "6385598" "6075338" "6424957"	EPO; JPO;	
		"5943664" "5806051" "5796917" "5710867"	DERWENT;	
		"5615303").did.)	IBM_TDB	
-	0	((fuzzy and min and max and circuit) and subtracter) and	USPAT;	2004/05/08 19:54
		mux and (("20020099673" "20020052857" "20020059149"	US-PGPUB;	
		"6424958" "5915247" "5875438" "6385598" "6075338"	EPO; JPO;	
		"6424957" "5943664" "5806051" "5796917" "5710867"	DERWENT;	
		"5615303").did.)	IBM_TDB	
-	1	((fuzzy and min and max and circuit) and subtracter) and	USPAT;	2004/05/08 20:02
		multiplexer and (("20020099673" "20020052857"	US-PGPUB;	
		"20020059149" "6424958" "5915247" "5875438" "6385598"	EPO; JPO;	
		"6075338" "6424957" "5943664" "5806051" "5796917"	DERWENT;	ļ
		"5710867" "5615303").did.)	IBM_TDB	
-	19615	subtract\$2 and compar\$4 and (mux or multiplex\$2)	USPAT;	2004/05/08 21:53
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2	(subtract\$2 and compar\$4 and (mux or multiplex\$2)) and	USPAT;	2004/05/08 20:03
		(("20020099673" "20020052857" "20020059149" "6424958"	US-PGPUB;	
		"5915247" "5875438" "6385598" "6075338" "6424957"	EPO; JPO;	
		"5943664" "5806051" "5796917" "5710867"	DERWENT;	
		"5615303").did.)	IBM_TDB	
-	15	PAPPALARDO-FRANCESCO.in.	USPAT;	2004/05/08 21:52
			US-PGPUB	' '
-	17	GIACALONE-BIAGIO.in.	USPAT;	2004/05/08 21:52
			US-PGPUB	, .
-	1	MAMMOLITI-FRANCESCO.in.	USPAT;	2004/05/08 21:53
			US-PGPUB	, ,
<b>-</b>	2	GANGI-EDMONDO.in.	USPAT;	2004/05/08 21:53
	1		US-PGPUB	·
-	4	subtract\$2 and compar\$4 and (mux or multiplex\$2) and	USPAT;	2004/05/08 21:54
	j	PAPPALARDO-FRANCESCO.in.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	subtract\$2 and compar\$4 and (mux or multiplex\$2) and	USPAT;	2004/05/08 21:54
		GIACALONE-BIAGIO.in.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	subtract\$2 and compar\$4 and (mux or multiplex\$2) and	USPAT;	2004/05/08 21:54
		MAMMOLITI-FRANCESCO.in.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	subtract\$2 and compar\$4 and (mux or multiplex\$2) and	USPAT;	2004/05/08 21:54
1		GANGI-EDMONDO.in.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

Ŭ.

izee home } search izee | shop | web account | contact izee



Membership	Publications/Services	Standards	Conferences	Careers/Jebs
	E Xplore	).	United States Pr	Welcome stent and Traden

Welcome United States Patent and Trademark Office



Help FAQ Terms IEI	E Peer Review	Quick Links	» Se:
Welcome to IEEE Xpiore®			
O- Home O- What Can I Access? O- Log-out			cuments. <b>15</b> to a page, sorted by <b>Relevanc</b> :
Fables of Contents	You may	is Search: efine your search by editing the the text box.	current search expression or enter

& Magazines

**Proceedings** 

Conference

( )- Standards

O- By Author

( )- Advanced

Member Services

Join IEEE

( )- Access the

Print Format

( )- Establish IEEE

Web Account

IEEE Member

Digital Library

O- Basic

Search

fuzzy<and>logic<and>circuit Search

Check to search within this result set

**Results Key:** 

**JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Synthesis of current mode building blocks for fuzzy logic control cir Patyra, M.J.; Long, J.E.; Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium

on , Volume: 4 , 30 May-2 June 1994

Pages:283 - 286 vol.4

[PDF Full-Text (344 KB)] [Abstract]

**2 CMOS fuzzy logic controller in current mode** 

Lemaitre, L.; Patyra, M.J.; Mlynek, D.; Custom Integrated Circuits Conference, 1993., Proceedings of the IEEE 1993

Pages: 25.8.1 - 25.8.3

May 1993

[Abstract] [PDF Full-Text (292 KB)] **IEEE CNF** 

3 Programmable fuzzy logic controller circuit on CPLD chip

Samman, F.A.; Syamsuddin, E.Y.;

Circuits and Systems, 2002. APCCAS '02. 2002 Asia-Pacific Conference

on, Volume: 2, 28-31 Oct. 2002

Pages: 561 - 564 vol. 2

[PDF Full-Text (422 KB)] [Abstract] **IEEE CNF** 

4 A fuzzy membership function circuit using hysteretic resonant tunn diodes

Hao Tang; Hung Chang Lin;

Multiple-Valued Logic, 1995. Proceedings., 25th International Symposium on 25 May 1995

Pages:182 - 186

~~

#### [PDF Full-Text (348 KB)] IEEE CNF [Abstract]

### 5 A fuzzy partitioning system

Rao Valavala, K.; Manzoul, M.A.;

Micro, IEEE, Volume: 15, Issue: 6, Dec. 1995

Pages:66

[Abstract] [PDF Full-Text (92 KB)] **IEEE JNL** 

### 6 Hardware fuzzy logic kit design

Gharieb, W.;

Fuzzy Systems, 1997., Proceedings of the Sixth IEEE International Conference

on , Volume: 2 , 1-5 July 1997

Pages:1039 - 1043 vol.2

[Abstract] [PDF Full-Text (364 KB)] **IEEE CNF** 

## 7 A fault model and a test method for analog fuzzy logic circuits

Weiner, S.;

Test Conference, 1995. Proceedings., International, 21-25 Oct. 1995.

Pages:282 - 291

[Abstract] [PDF Full-Text (876 KB)] **IEEE CNF** 

### 8 A fuzzy logic inference processor

Fattaruso, J.W.; Mahant-Shetti, S.S.; Brock Barton, J.;

Industrial Fuzzy Control and Intelligent Systems, 1993., IFIS '93., Third

International Conference on , 1-3 Dec. 1993

Pages:210 - 214

[Abstract] [PDF Full-Text (316 KB)] **IEEE CNF** 

## 9 Synthesis and design automation of analog fuzzy logic VLSI circuits

Lemaitre, L.; Patyra, M.J.; Mlynek, D.;

Multiple-Valued Logic, 1993., Proceedings of The Twenty-Third International

Symposium on , 24-27 May 1993

Pages:74 - 79

[Abstract] [PDF Full-Text (540 KB)] IEEE CNF

### 10 Piecewise linear macromodels for elementary logic and fuzzy circu

Tesu, I.C.; Dartu, F.;

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium

on , 3-6 May 1993

Pages:1718 - 1721 vol.3

[PDF Full-Text (276 KB)] [Abstract] **IEEE CNF** 

### 11 Fuzzy-logic-based analog design tools

Torralba, A.J.; Chavez, J.; Franquelo, L.G.;

Micro, IEEE, Volume: 16, Issue: 4, Aug. 1996

Pages:60 - 68

### [Abstract] [PDF Full-Text (684 KB)] IEEE JNL

12 Efficient fuzzy logic architectures suitable for silicon compilation Wicks, T.; Nigri, M.; Treleaven, P.;

Proceedings of ISUMA - NAFIPS '95 The Third International Symposium on Uncertainty Modeling and Analysis and Annual Conference of the North Ameri Fuzzy Information Processing Society , 17-20 Sept. 1995

Pages:363 - 368

[Abstract] [PDF Full-Text (532 KB)] IEEE CNF

13 Application of fuzzy logic in resistive fault modeling and simulation Nourani, M.; Attarha, A.R.; Lucas, C.;

Fuzzy Systems, IEEE Transactions on , Volume: 10 , Issue: 4 , Aug. 2002 Pages: 461 - 472

[Abstract] [PDF Full-Text (373 KB)] IEEE JNL

14 Analysis and design of CMOS fuzzy logic controller in current mode Lemaitre, L.; Patyra, M.J.; Mlynek, D.;

Solid-State Circuits, IEEE Journal of , Volume: 29 , Issue: 3 , March 1994 Pages: 317 - 322

[Abstract] [PDF Full-Text (520 KB)] IEEE JNL

### 15 Analog gates for a VLSI fuzzy processor

Catania, V.; Russo, M.;

VLSI Design, 1995., Proceedings of the 8th International Conference on , 4-7 1995

Pages: 299 - 304

[Abstract] [PDF Full-Text (544 KB)] IEEE CNF

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 2: 25 26 27 28 29 30 31 32 33 34 Next

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account |
New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online
Publications | Help | FAQ| Terms | Back to Too

Copyright © 2004 IEEE - All rights reserved

Advanced

Member Services

( ) Join IEEE

Access the

🚟 Print Format

( )- Establish IEEE

Web Account

IEEE Member

Digital Library

ieee home | Search ieee | Shop | Web account | Contact ieee



					·
Membership	Publicati	onsiservices	Standards	Conferences	Careers/Jobs
	ΞX	plore	)	inited States Pa	Welcome test and Trades
	rms IEEE I	Paer Review	Quick Lin	ks	<b>S</b>
Welcome to IEE	2.0000				
O- Home O- What Can I Access?			n of <b>500</b> r		<b>809</b> documen played, <b>15</b> to

Welcome



United States Patent and Trademark Office » Se: ck Links tched 28 of 1034809 documents. 500 results are displayed, 15 to a page, sorted by Relevance e**scending** order. O-Log-out Refine This Search: Tables of Contents You may refine your search by editing the current search expression or enter new one in the text box. ( )- Journals & Magazines Search fuzzy<and>min<and>max<and>circuit ()- Conference Check to search within this result set Proceedings ( )- Standards **Results Key: JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard Search O- By Author ( )- Basic

1 Implementation of O(n) complexity max/min circuits for fuzzy and connectionist computing

Hassoun, M.H.; Nabha, A.M.;

Neural Networks, 1993., IEEE International Conference on , 28 March-1 April Pages:998 - 1003 vol.2

[PDF Full-Text (444 KB)] [Abstract] **IEEE CNF** 

2 Pointer adaptation and pruning of min-max fuzzy inference and estimation

Arabshahi, P.; Marks, R.J., II; Seho Oh; Caudell, T.P.; Choi, J.J.; Bong-Gee S Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transaction [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Vol. 44 , Issue: 9 , Sept. 1997

Pages:696 - 709

[PDF Full-Text (500 KB)] [Abstract] **IEEE JNL** 

3 Design and preliminary results of high speed analog 1.0 µm CMOS N MAX circuit for fuzzy architectures

Gabrielli, A.; Gandolfi, E.; Masetti, M.; Maloberti, F.;

Circuits and Systems, 1995., Proceedings., Proceedings of the 38th Midwest Symposium on , Volume: 1 , 13-16 Aug. 1995

Pages:381 - 384 vol.1

[PDF Full-Text (348 KB)] [Abstract] **IEEE CNF** 

4 Design of MIN/MAX cellular neural networks (MMCNNS) in CMOS

Wen-Cheng Yen; Rong-Jian Chen; Jui-Lin Lai;

Cellular Neural Networks and Their Applications, 2002. (CNNA 2002). Proceed of the 2002 7th IEEE International Workshop on , 22-24 July 2002 Pages:339 - 346

[Abstract] [PDF Full-Text (340 KB)] **IEEE CNF** 

### 5 Fuzzy aggregating functions for multiobjective VLSI placement

Khan, J.A.; Sait, S.M.;

Fuzzy Systems, 2002. FUZZ-IEEE'02. Proceedings of the 2002 IEEE Internation Conference on , Volume: 2 , 12-17 May 2002

Pages:831 - 836

[Abstract] [PDF Full-Text (548 KB)] **IEEE CNF** 

### 6 Implementing a fuzzy inference engine using FPGA

Hung, D.; Zajac, W.;

ASIC Conference and Exhibit, 1993. Proceedings., Sixth Annual IEEE International, 27 Sept.-1 Oct. 1993

Pages: 349 - 352

[Abstract] [PDF Full-Text (288 K8)] IEEE CNF

### 7 The concept of fuzzy flip-flop

Hirota, K.; Ozawa, K.;

Systems, Man and Cybernetics, IEEE Transactions on , Volume: 19 , Issue:

5 , Sept.-Oct. 1989

Pages:980 - 997

[Abstract] [PDF Full-Text (888 KB)] **IEEE JNL** 

### 8 The synthesis of compact fuzzy neural circuits

Hurdle, J.F.;

Fuzzy Systems, IEEE Transactions on , Volume: 5 , Issue: 1 , Feb. 1997

Pages:44 - 55

[Abstract] [PDF Full-Text (200 KB)] **IEEE JNL** 

### 9 Evaluation of min/max instructions for fuzzy information processing

Watanabe, H.; Chen, D.; Konuri, S.;

Fuzzy Systems, IEEE Transactions on , Volume: 4 , Issue: 3 , Aug. 1996

Pages:369 - 374

[Abstract] [PDF Full-Text (472 KB)] IEEE JNL

#### 10 A multilevel systolic approach for fuzzy inference hardware

de Salvador, L.; Gutierrez, J.;

Micro, IEEE, Volume: 15, Issue: 5, Oct. 1995

Pages:61 - 71

[PDF Full-Text (724 KB)]

## 11 CCII-based fuzzy membership function and max/min circuits

Liu, S.I.; Hwang, Y.S.; Tsay, J.H.;

• Search Results Page 3 of 3

Electronics Letters, Volume: 29, Issue: 1, 7 Jan 1993

Pages:116

[Abstract] [PDF Full-Text (216 KB)] IEE JNL

### 12 Hardware fuzzy logic kit design

Gharieb, W.;

Fuzzy Systems, 1997., Proceedings of the Sixth IEEE International Conference

on , Volume: 2 , 1-5 July 1997

Pages:1039 - 1043 vol.2

[Abstract] [PDF Full-Text (364 KB)] IEEE CNF

# 13 A reconfigurable parallel inference processor for high speed fuzzy systems

Lees, M.J.; Campbell, D.A.; Devlin, J.C.;

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World'., 1996 IEEE

International Symposium on , Volume: 3 , 12-15 May 1996

Pages:539 - 542 vol.3

[Abstract] [PDF Full-Text (248 KB)] IEEE CNF

### 14 Fuzzy Petri nets for rule-based pattern classification

Xi Chen; Dongming Jin; Zhijian Li;

Communications, Circuits and Systems and West Sino Expositions, IEEE 2002

International Conference on , Volume: 2 , 29 June-1 July 2002

Pages:1218 - 1222 vol.2

[Abstract] [PDF Full-Text (367 KB)] IEEE CNF

## 15 Recursive training for multi-resolution fuzzy min-max neural network classifier

Chen Xi; Jin Dongming; Li Zhijian;

Solid-State and Integrated-Circuit Technology, 2001. Proceedings. 6th

International Conference on , Volume: 1 , 22-25 Oct. 2001

Pages:131 - 134 vol.1

[Abstract] [PDF Full-Text (230 KB)] IEEE CNF

1 2 Next

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account |
New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online
Publications | Help | FAQ| Terms | Back to Ten

Copyright © 2004 IEEE - All rights reserved

– Basic

Member Services

O Join IEEE

O- Access the

Print Format

( )- Establish lEEE Web Account

IEEE Member

Digital Library

Advanced

TEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership	Publications/Services	313818
		*******
	REFEREN	

tards Conferences Careers/Jobs

Welcome united States Batest and Trademark Office



	CHECKE IV
Help FAQ Terms	IEEE Peer Review Quick Links "Se
Welconse to IEEE Xpio	
O- Home	Your search matched 28 of 1034809 documents.
O- What Can I Access?	A maximum of <b>500</b> results are displayed, <b>15</b> to a page, sorted by <b>Relevance Descending</b> order.
O- Log-out	Descending order.
Tables of Contents	Refine This Search: You may refine your search by editing the current search expression or enter
O- Journals & Magazines	new one in the text box.
	fuzzy <and>min<and>max<and>circuit Search</and></and></and>
O- Conference Proceedings	Check to search within this result set
O-Standards	Results Key:
Search	JNL = Journal or Magazine CNF = Conference STD = Standard
O- By Author	

### 16 A generalized high-precision analog CMOS rank finder for max/min/med application

Yu-Cherng Hung; Bin-Da Liu;

Fuzzy Systems Conference Proceedings, 1999. FUZZ-IEEE '99. 1999 IEEE

International, Volume: 3, 22-25 Aug. 1999

Pages:1680 - 1684 vol.3

[Abstract] [PDF Full-Text (204 KB)] **IEEE CNF** 

### 17 Mixed-signal CMOS fuzzifier with emphasis on power consumption Carvajal, R.G.; Torralba, A.; Colodro, F.; Franquelo, L.G.;

Circuits and Systems, 1999. 42nd Midwest Symposium on , Volume: 2 , 8-11

1999

Pages:929 - 933 vol. 2

[Abstract] [PDF Full-Text (284 KB)] **IEEE CNF** 

### 18 Analog VLSI hardware for fuzzy systems

Wilamowski, B.M.;

Industrial Electronics Society, 1998. IECON '98. Proceedings of the 24th Annu Conference of the IEEE, Volume: 1, 31 Aug.-4 Sept. 1998

Pages:52 - 55 vol.1

[Abstract] [PDF Full-Text (316 K8)] **IEEE CNF** 

### 19 Evaluation of fuzzy instructions in a RISC processor

Watanabe, H.; Chen, D.;

Fuzzy Systems, 1993., Second IEEE International Conference on , 28 March-: 1993

Pages:521 - 526 vol.1

### [Abstract] [PDF Full-Text (400 KB)] IEEE CNF

# 20 A design of current-mode analog circuits for fuzzy inference hardw systems

Tsukano, K.; Inoue, T.; Ueno, F.; Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993 Pages:1385 - 1388 vol.2

[Abstract] [PDF Full-Text (256 KB)] IEEE CNF

# 21 Architecture of a CMOS fuzzy logic controller with optimized memo organisation and operator design

Eichfeld, H.; Lohner, M.; Muller, M.; Fuzzy Systems, 1992., IEEE International Conference on , 8-12 March 1992 Pages:1317 - 1323

[Abstract] [PDF Full-Text (356 KB)] IEEE CNF

### 22 A fuzzy programmable logic array (fuzzy PLA)

Yamakawa, T.;

Fuzzy Systems, 1992., IEEE International Conference on , 8-12 March 1992 Pages: 459 - 465

[Abstract] [PDF Full-Text (300 KB)] IEEE CNF

# 23 Fuzzy multiple-input maximum and minimum circuits in current morand their analyses using bounded-difference equations

Sasaki, M.; Inoue, T.; Shirai, Y.; Ueno, F.; Computers, IEEE Transactions on , Volume: 39 , Issue: 6 , June 1990 Pages: 768 - 774

[Abstract] [PDF Full-Text (464 KB)] IEEE JNL

# 24 Continuous-time analog defuzzifier for product-sum based implementations

Rojas, I.; Pelayo, F.J.; Anguita, M.; Prieto, A.; Microelectronics for Neural Networks and Fuzzy Systems, 1994., Proceedings Fourth International Conference on , 26-28 Sept. 1994 Pages: 324 - 330

[Abstract] [PDF Full-Text (368 KB)] IEEE CNF

# $_{\rm 25}$ Architecture of a 50 MFIPS fuzzy processor and the related 1 $\mu m$ V CMOS digital circuits

Gandolfi, E.; Masetti, M.; D'Antone, I.; Gabrielli, A.; Spotti, M.; Microelectronics for Neural Networks and Fuzzy Systems, 1994., Proceedings Fourth International Conference on , 26-28 Sept. 1994 Pages: 125 - 133

[Abstract] [PDF Full-Text (632 KB)] IEEE CNF

### 26 Efficient analog CMOS implementation of fuzzy rules by direct synt of multidimensional fuzzy subspaces

Landolt, O.;

Fuzzy Systems, 1993., Second IEEE International Conference on, 28 March-: 1993

Pages:453 - 458 vol.1

[Abstract] [PDF Full-Text (412 KB)] IEEE CNF

### 27 Piecewise linear macromodels for elementary logic and fuzzy circu

Tesu, I.C.; Dartu, F.;

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

Pages:1718 - 1721 vol.3

[Abstract] [PDF Full-Text (276 KB)] **IEEE CNF** 

### 28 Analysis and design of analog CMOS building blocks for integrated inference circuits

Inoue, T.; Ueno, F.; Motomura, T.; Matsuo, R.; Setoguchi, O.; Circuits and Systems, 1991., IEEE International Sympoisum on , 11-14 June Pages: 2024 - 2027 vol.4

[PDF Full-Text (232 KB)] [Abstract] **IEEE CNF** 

<u>Prev 1 2</u>

home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top

Copyright © 2004 IEEE — All rights reserved